

SPECIFICATION

Device Name : SILICON DIODE

Type Name : TP802C04R

Spec. No. : MS5D3004

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				DWGNO. MS5D3004 1/11

1. SCOPE

This specification provides the ratings and the test requirement for FUJI SILICON DIODE TP802C04R

2. OUT VIEW , MARKING , MOLDING RESIN , CHARACTERISTICS

- (1) Out view is shown MS5D3004 8/11
- (2) Marking is shown MS5D3004 8/11
It is marked to type name or abbreviated type name, polarity and Lot No.
- (3) Molding resin
Epoxy resin UL:V-0
- (4) Characteristics is shown MS5D3004 9/11~11/11

Bar Code Label of EIAJ C-3 Specification. Indispensable description items are shown as below.

- (1) Type Name
- (2) Production Code
- (3) Quantity
- (4) Lot No.(Date code)
- (5) Company Code

3. RATINGS

3.1 MAXIMUM RATINGS (at Ta=25°C unless otherwise specified.)

ITEM	SYMBOL	CONDITIONS	RATINGS	UNITS
Repetitive peak surge reverse voltage	V_{RSM}	tw=500ns,duty=1/40	48	V
Repetitive peak reverse voltage	V_{RRM}		40	V
Average output current	I_o	50Hz Square wave duty =1/2 Tc = 116°C	10 *	A
Non-repetitive forward surge current**	I_{FSM}	Sine wave, 10ms 1shot	120	A
Operating junction temperature	Tj		150	°C
Storage temperature	Tstg		-40~ +150	°C

* Out put current of center tap full wave connection.

**Rating per element

3.2 ELECTRICAL CHARACTERISTICS (at Ta=25°C unless otherwise specified.)

ITEM	SYMBOL	CONDITIONS	MAXIMUM	UNITS
Forward voltage ***	V_F	$I_F = 4 A$	0.55	V
Reverse current ***	I_R	$V_R = V_{RRM}$	5	mA
Thermal resistance	Rth(j-c)	Junction to case	3	°C/W

*** Rating per element

3.3 MECHANICAL CHARACTERISTICS

Approximate mass		1.6	g
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MS5D3004 3/11

4. TEST AND INSPECTION

4.1 STANDARD TEST CONDITION

Standard test condition is Ta=25°C、65%R.H.

If judgment is no doubt, the test condition is possible to test in normal condition

Ta=5~35°C、48~85%R.H.

4.2 STRUCTURE INSPECTION

It inspect with eye and measure, Item 2 shall be satisfied.

4.3 FORWARD AND REVERSE CHARACTERISTICS

It inspect on the standard condition, Item 3.2 shall be satisfied.

4.4 TEST

	Test No.	Test Items	Testing methods and Conditions	Reference Standard EIAJ ED4701	Sampling number	Acceptance number
Mechanical test	1	Terminal Strength (Tensile)	Pull force : 10N Force maintaining duration :10±1s	EIAJ ED4701/401 method 1	5	(0 : 1)
	2	Terminal Strength (Bending)	Load force : 5N Number of times : 2times(90deg./time)	EIAJ ED4701/401 method 3	5	
	3	Vibration	Frequency : 100Hz to 2kHz Acceleration : 100m/s ² Sweeping time : 4min./1 cycle 4times for each X, Y&Z directions.	EIAJ ED4701/403 test code D	5	
	4	Shock	Peak amplitude : 15km/s ² Duration time : 0.5ms 3times for each X, Y&Z directions.	EIAJ ED4701/404 test code D	5	
	5	Solder ability 1	Solder : Sn-37Pb Solder temp. : 235±5°C Immersion time : 5±0.5s Apply to flux	EIAJ ED4701/303 test code A	5	
		Solder ability 2	Solder : Sn-3Ag-0.5Cu Solder temp. : 245±5°C Immersion time : 5±0.5s Apply to flux	—————	5	
	6	Resistance to Soldering Heat	Solder temp. : 260±5°C Immersion time : 10±1s Number of times : 1times	EIAJ ED4701/302 test code A	5	

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	Test No.	Test Items	Testing methods and Conditions	Reference Standard EIAJ ED4701	Sampling number	Acceptance number
Endurance test	1	High Temp. Storage	Temperature :Tstg max Test duration : 1000h	EIAJ ED4701/201	22	(0 : 1)
	2	Low Temp. Storage	Temperature :Tstg min Test duration : 1000h	EIAJ ED4701/202	22	
	3	Temperature Humidity Storage	Temperature : 85±2°C Relative humidity : 85±5% Test duration : 1000h	EIAJ ED4701/103 test code C	22	
	4	Temperature Humidity Bias	Temperature : 85±2°C Relative humidity : 85±5% Bias Voltage : V _{RRM} × 0.8 Test duration : 1000h	EIAJ ED4701/103 test code C	22	
	5	Unsaturated Pressurized Vapor	Temperature : 130±2°C Relative humidity : 85±5% Vapor pressure : 230kPa Test duration : 48h	EIAJ ED4701/103 test code F	22	
	6	Temperature Cycle	High temp. side : Tstg max Room temp. : 5~35°C Low temp. side : Tstg min Duration time : HT 30min,RT 5min LT 30min Number of cycles : 100 cycles	EIAJ ED4701/105	22	
	7	Thermal Shock	Fluid : pure water(running water) High temp. side : 100+0/-5°C Low temp. side : 0+5/-0°C Duration time : HT 5min,LT 5min Number of cycles : 100 cycles	EIAJ ED4701/307 test code A	22	
	8	Steady state Operating life	Ta=25±5°C Rated load Test duration : 1000h	—————	22	
	9	Intermittent Operating life	Tj=Tjmax ~50°C 3min ON, 3min OFF Test duration : 10000cycles	EIAJ ED4701/106	22	
	10	High Temp. Reverse Bias	Temperature : Ta=100 °C Bias Voltage : VR=V _{RRM} duty=1/2 Test duration : 1000h	EIAJ ED4701/101	22	

Failure Criteria	$I_R \leq USL \times 2$	USL : Upper specification Limit
	$V_F \leq USL \times 1.1$	

Installation

- Soldering involves temperatures which exceed the device storage temperature rating. To avoid device damage and to ensure reliability, observe the following guidelines from the quality assurance standard.

Table 1: Solder temperature and duration

Method	Solder temperature	Duration
Flow	260±5°C	10±1sec
Soldering iron	350±10°C	3.5±0.5sec

- The immersion depth of the lead should basically be up to the lead stopper and the distance should be a maximum of 1.5mm from the device.
- When flow-soldering, be careful to avoid immersing the package in the solder bath.

Storage

- The Diodes must be stored at a standard temperature of 5 to 35°C and relative humidity of 45 to 75%.If the storage area is very dry, a humidifier may be required. In such a case, use only deionized water or boiled water, since the chlorine in tap water may corrode the leads.
- The Diodes should not be subjected to rapid changes in temperature to avoid condensation on the surface of the Diodes. Therefore, store the Diodes in a place where the temperature is steady.
- The Diodes should not be stored on top of each other, since this may cause excessive external force on the case.
- The Diodes should not be stored with the lead terminals remaining unprocessed. Rust may cause presoldered connections to go fail during later processing.
- The Diodes should be stored in antistatic containers or shipping bags.

7.Appendix

- This products does not contain PBBs (Polybrominated Biphenyl) or PBDEs (Polybrominated Diphenyl Ether) , substances.
- This products does not contain Class-I ODS and Class-II ODS substances set force by 'Clean Air Act of US' law.

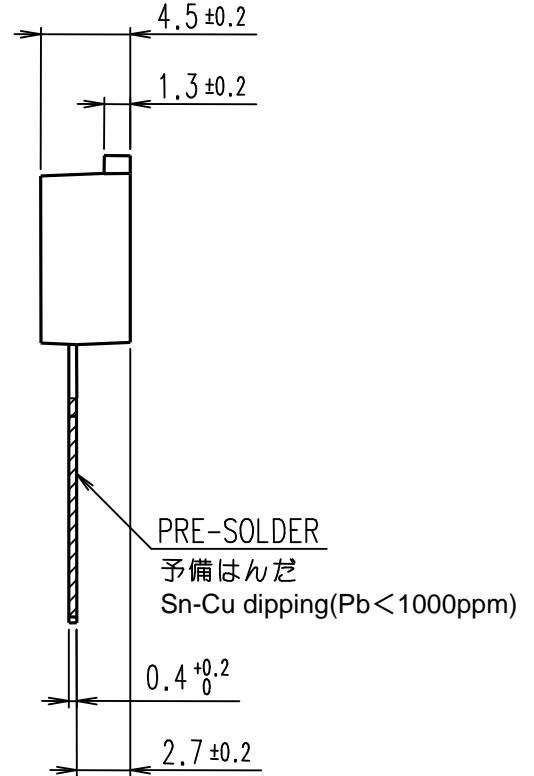
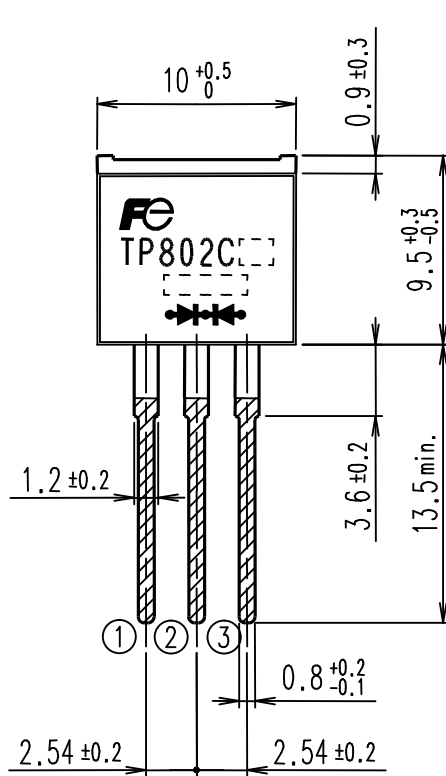
- If you have any questions about any part of this Specification, please contact Fuji Electric Device Technology or its sales agent before using the product
 - Neither Fuji nor its agents shall be held liable for any injury caused by using the products not in accordance with the instructions.
 - The application examples described in this specification are merely typical uses of Fuji Electric DeviceTechnology products.
- This specification does not confer any industrial property rights or other rights, nor constitute a license for such rights.

FUJI SILICON DIODE

TYPE : TP802C□□R

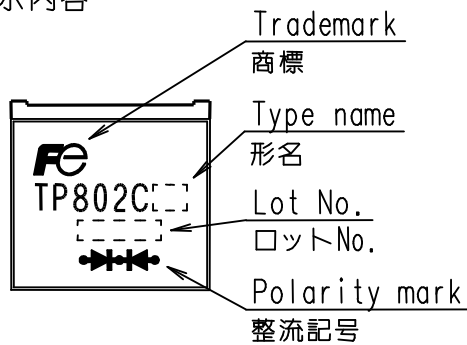
OUT VIEW

外形寸法図



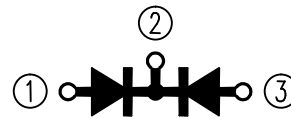
MARKING

表示内容



CONNECTION

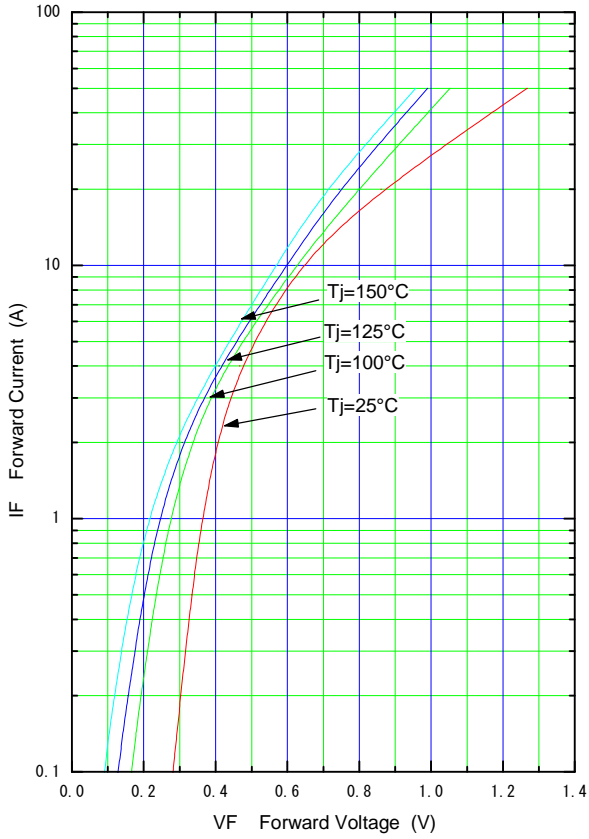
結線図



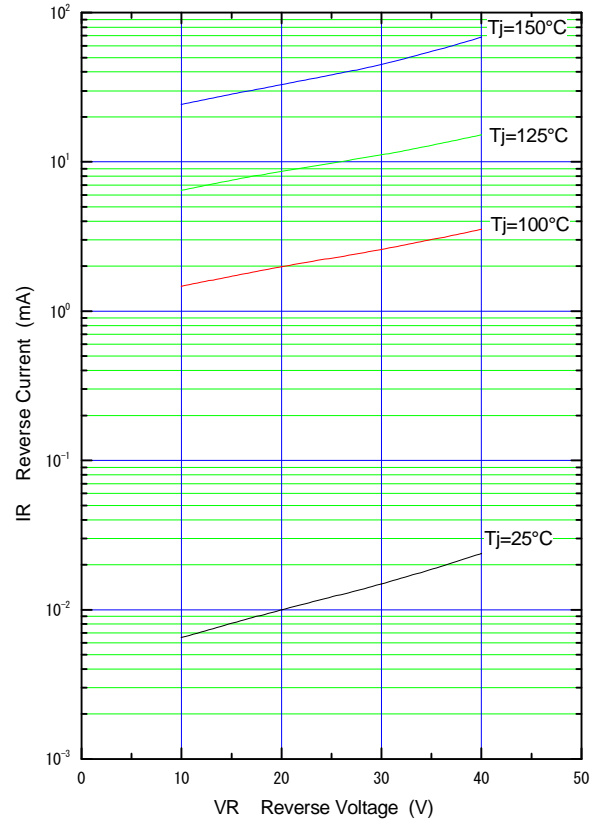
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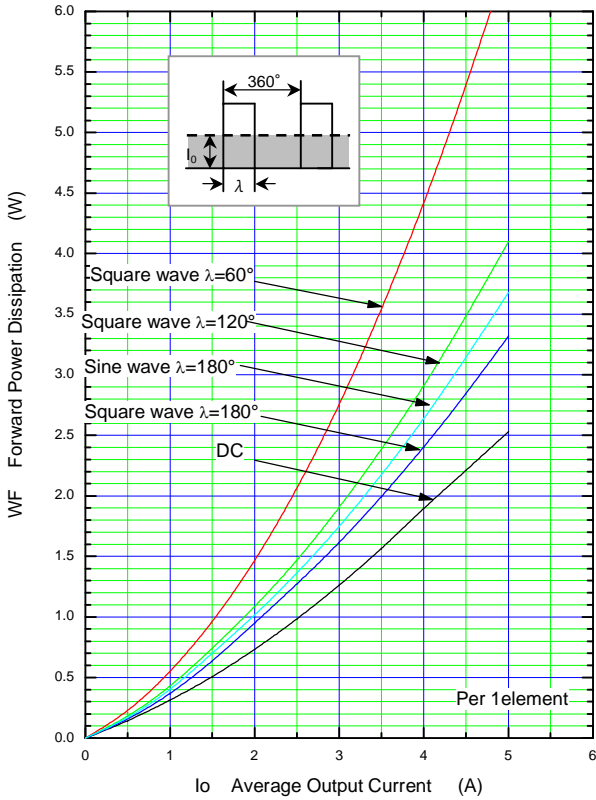
Forward Characteristic (typ.)



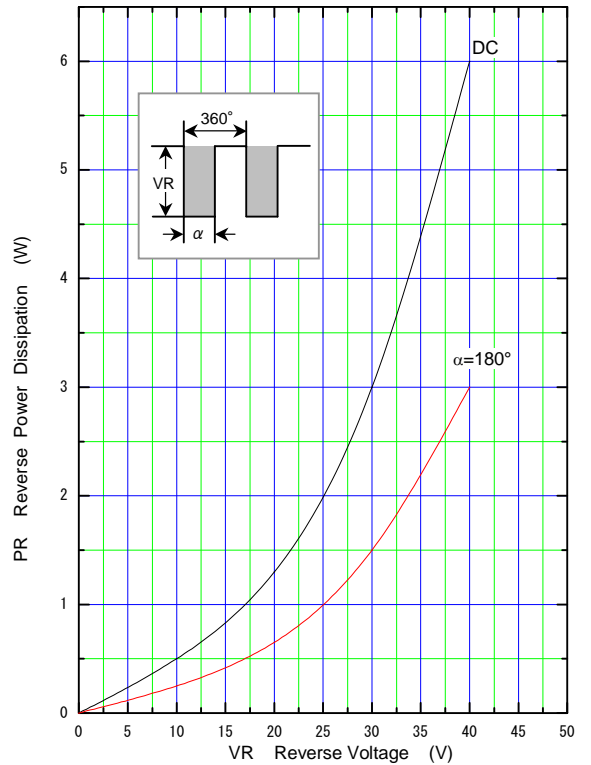
Reverse Characteristic (typ.)



Forward Power Dissipation (max.)

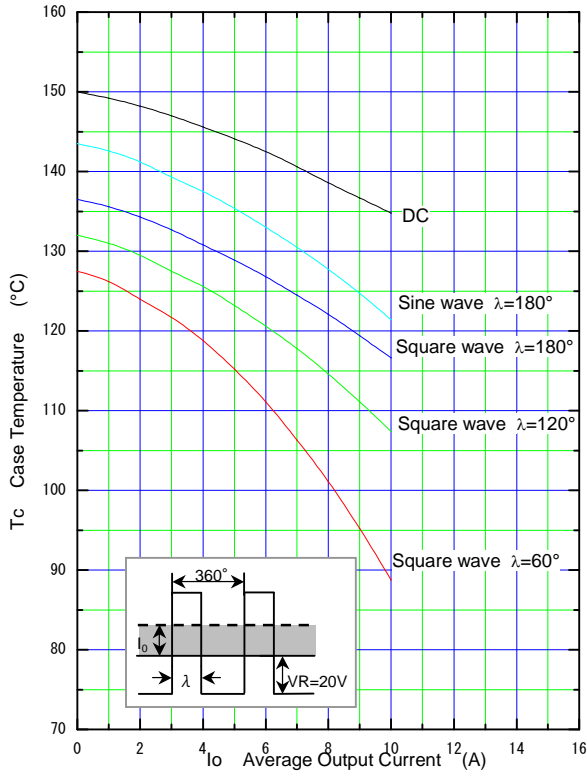


Reverse Power Dissipation (max.)



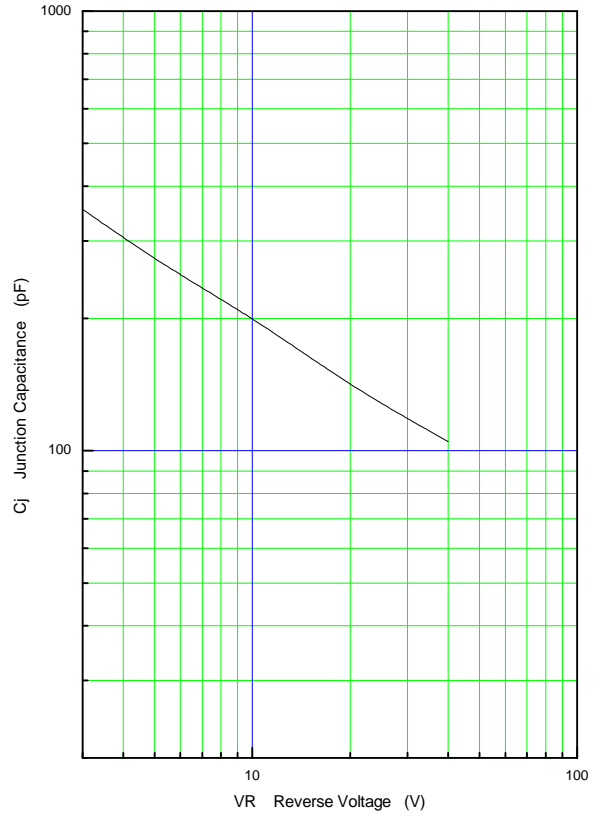
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Current Derating (Io-Tc) (max.)

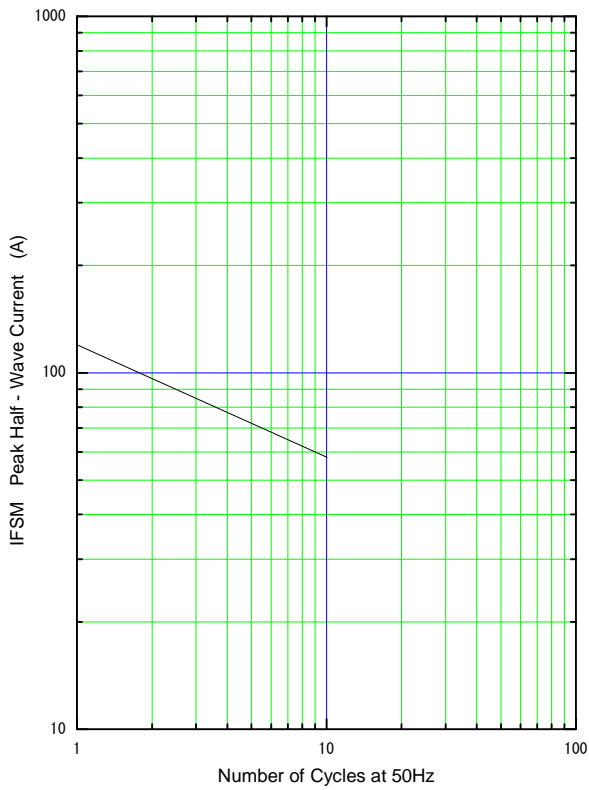


λ: Conduction angle of forward current for each rectifier element
 Io: Output current of center-tap full wave connection

Junction Capacitance Characteristic (typ.)



Surge Capability (max.)



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Transient Thermal Impedance (max.)

